

Device Target Output Listing User C/C++ Asm Linker Debug Utilities

Dialog Semiconductor DA14580

Xtal (MHz): 16.0

Operating system: None

System Viewer File:

DA14580.sfr

 Use Custom File

Code Generation

ARM Compiler: Use default compiler version

 Use Cross-Module Optimization Use MicroLIB Big Endian

Read/Only Memory Areas

default	off-chip	Start	Size	Startup
<input type="checkbox"/>	ROM1:			<input type="radio"/>
<input type="checkbox"/>	ROM2:			<input type="radio"/>
<input type="checkbox"/>	ROM3:			<input type="radio"/>
	on-chip			
<input checked="" type="checkbox"/>	IROM1:	0x0	0x57BC	<input checked="" type="radio"/>
<input type="checkbox"/>	IROM2:			<input type="radio"/>

Read/Write Memory Areas

default	off-chip	Start	Size	NoInit
<input type="checkbox"/>	RAM1:			<input type="checkbox"/>
<input type="checkbox"/>	RAM2:			<input type="checkbox"/>
<input type="checkbox"/>	RAM3:			<input type="checkbox"/>
	on-chip			
<input checked="" type="checkbox"/>	IRAM1:	0x57BC	0x5044	<input type="checkbox"/>
<input type="checkbox"/>	IRAM2:			<input type="checkbox"/>

OK

Cancel

Defaults

Help